

80S32 Validation Board

!- @page { margin: 0.79in } P { margin-bottom: 0.08in } --> The 80S32 validation board was specifically developed for performing functional and performance validation of the 80S32 microcontroller, under an ESA contract. The FPGA based architecture of the board includes various components and peripheral devices for exercising the microcontroller under different conditions of varying voltage, temperature and operating frequency. A successful radiation campaign was performed using the board to determine the radiation sensitivity of engineering devices against Single Event Upsets (SEU).

<http://microelectronics.esa.int/components/comppage.htm#80S32>

<http://microelectronics.esa.int/cgi-bin/finalreport.cgi>

{gallery}products/ISD_product_info_web/80S32_validation_board/pics{/gallery}

80S32 Validation Board key features

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FPGA based architecture configured to act as a traffic handler between the various peripheral interfaces or providing stimuli to the microcontroller.

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Socketed device under test.

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Various memory devices: DDR, Flash, SRAM.

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Five UART interfaces.

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Programmable user interfaces: switches, LEDs, LCD screen, keypad.

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Reconfigurable, multiple output power supply section.

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PCI-X compatible card edge connection with embedded PCI-X IP core.